



Greg Michelson

Partner

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PRACTICES Intellectual Property, Patents, Patent Prosecution and Counseling, Patent Office Trials, Autonomous Transportation, International Trade Commission Section 337 Proceedings

Greg Michelson is the managing partner of the Orange County office of Haynes and Boone and a partner in the Intellectual Property Practice Group. Greg provides strategic counseling, patent portfolio management, licensing, and has experience with post grant proceedings and IP litigation. He provides guidance in all aspects of intellectual property matters, including due diligence, opinions, as well as patent prosecution and litigation. Greg manages large, high technology patent portfolios and has prepared and prosecuted U.S. and foreign patent applications covering a wide variety of complex technologies, involving technology areas such as electrical devices, semiconductor processes, software and computer architectures, communication systems, medical devices, and mechanical devices. He also represents clients in *inter partes* review (IPR) proceedings at the Patent and Trademark Office. Greg supports patent litigations, including matters before the International Trade Commission (ITC) and federal district courts, for all phases of litigation from initial pre-filing analysis, to filing, discovery, depositions, expert reports, direct and cross-examinations, and briefs through trial.

In addition to his extensive legal experience and educational background in electrical engineering, Greg has more than seven years of technical engineering experience at Hughes Aircraft Company and TRW. At Hughes Aircraft Company, he performed hardware and software design analysis and test verification and development, for aircraft radar, communication, and associated systems. Greg was a systems engineer for satellite development at TRW where he performed system development and test along with payload performance analysis for complex communication systems.

In addition to his law degree, Greg holds M.S.E.E. and B.S.E.E. degrees in electrical engineering.

QUALIFICATIONS

EDUCATION

- J.D., Loyola Law School Los Angeles, 1998, *cum laude*; (top 5% of class); Order of the Coif; Editor, *Loyola of Los Angeles Law Review*
- Electrical Engineer, M.S.E.E., Communications and Signal/Image Processing, University of Southern California, 1992
- Electrical Engineer, B.S.E.E., California State Polytechnic University, 1988, *cum laude*

ADMISSIONS

- California
- U.S. Patent and Trademark Office

COURT ADMISSIONS

- U.S. District Court for the Central District of California
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PUBLICATIONS AND SPEAKING ENGAGEMENTS

- "Patent Prosecution Panel," 4th Annual Intellectual Property Law Careers Event, speaker, San Diego, CA, March 29, 2011.
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PROFESSIONAL AFFILIATIONS AND ENGAGEMENTS

- State Bar of California
 - American Intellectual Property Law Association
 - Orange County Patent Law Association
 - Los Angeles Intellectual Property Law Association
 - Intellectual Property Owners Association
 - Institute of Electrical and Electronics Engineers
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SELECTED CLIENT REPRESENTATIONS

- *In re Field Programmable Gate Arrays (337-TA-441)*: Represented complainant Xilinx, Inc. in a patent infringement case involving field programmable gate arrays (FPGAs).
 - *UPI Semiconductor Corporation v. Richtek Technology Corporation (CA State and USDC ND Cal)*: Represented UPI Semiconductor in a patent infringement case involving power management integrated circuits.
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AWARDS AND RECOGNITIONS

- Recognized as a leading patent prosecution attorney by *Intellectual Asset Management (IAM) Patent 1000*, Globe Business Media Group, 2019-2024